

Docket No.: 56937-089



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Kenichiro UDA	:	Confirmation Number: 3242
Application No.: 10/658,195	:	Group Art Unit: 2818
Filed: September 10, 2003	:	Examiner: Unknown
For: STRUCTURE OF POWER SUPPLY PATH UTILIZED IN DESIGN OF INTEGRATED CIRCUIT		

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The Japanese reference, 03-250735, was first cited in a corresponding Japanese application office action and its relevance discussed therein. A copy of the foreign office action is attached for the Examiner's information.

10/658,195

Please charge any shortage in fees due in connection with the filing of this paper,  
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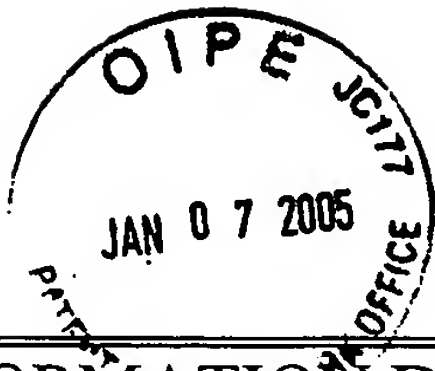
Respectfully submitted,

McDERMOTT WILL & EMERY LLP

Michael E. Fogarty  
Registration No. 36,139

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 MEF:tlg  
Facsimile: 202.756.8087  
**Date: January , 2005**

**Please recognize our Customer No. 20277  
as our correspondence address.**



SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  (PTO-1449)				ATTY. DOCKET NO. <b>56937-089</b>		SERIAL NO. <b>10/658,195</b>	
				APPLICANT <b>Kenichiro UDA</b>			
				FILING DATE <b>September 10, 2003</b>		GROUP <b>2818</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		US					
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<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number 4-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes      No
		JP 03-250735	11/8/1991	FUJITSU LTD FUJITSU VLSI LTD			w/English abstract
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
EXAMINER				DATE CONSIDERED			

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.